CUSTOMER NO.: 24498 Serial No.: 09/725,439

PATENT PU000142

IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) Apparatus, comprising:

enter 9-13-04 f playback of a first recorded

an input circuit for receiving a video signal indicative of playback of a first recorded medium;

said video signal, said detection circuit including a horizontal lock detector for determining whether said horizontal synchronization signal is received at appropriate intervals, said horizontal lock detector producing an error signal indicative of the absence of a valid horizontal synchronization signal, said detection circuit including an error counter, for counting the number of horizontal synchronization signal errors during a predefined time period and indicating whether a threshold level of horizontal synchronization signal errors has been exceeded; and

said video signal responsive to detecting said horizontal synchronizing signal.

- 2. (Cancelled).
- 3. (Cancelled)
- 4. (Original) The apparatus of claim 3, wherein said horizontal lock detector is responsive to a clocking signal.
- 5. (Previously Presented) The apparatus of claim 1, further comprising a vertical lock detector, for determining whether a vertical synchronization signal is received at an appropriate time interval.
- 6. (Original) The apparatus of claim 5, wherein said detection circuit further comprises a countdown timer, responsive to a received horizontal synchronization signal, for producing an output signal indicative of a time interval during which a vertical synchronization signal is expected.